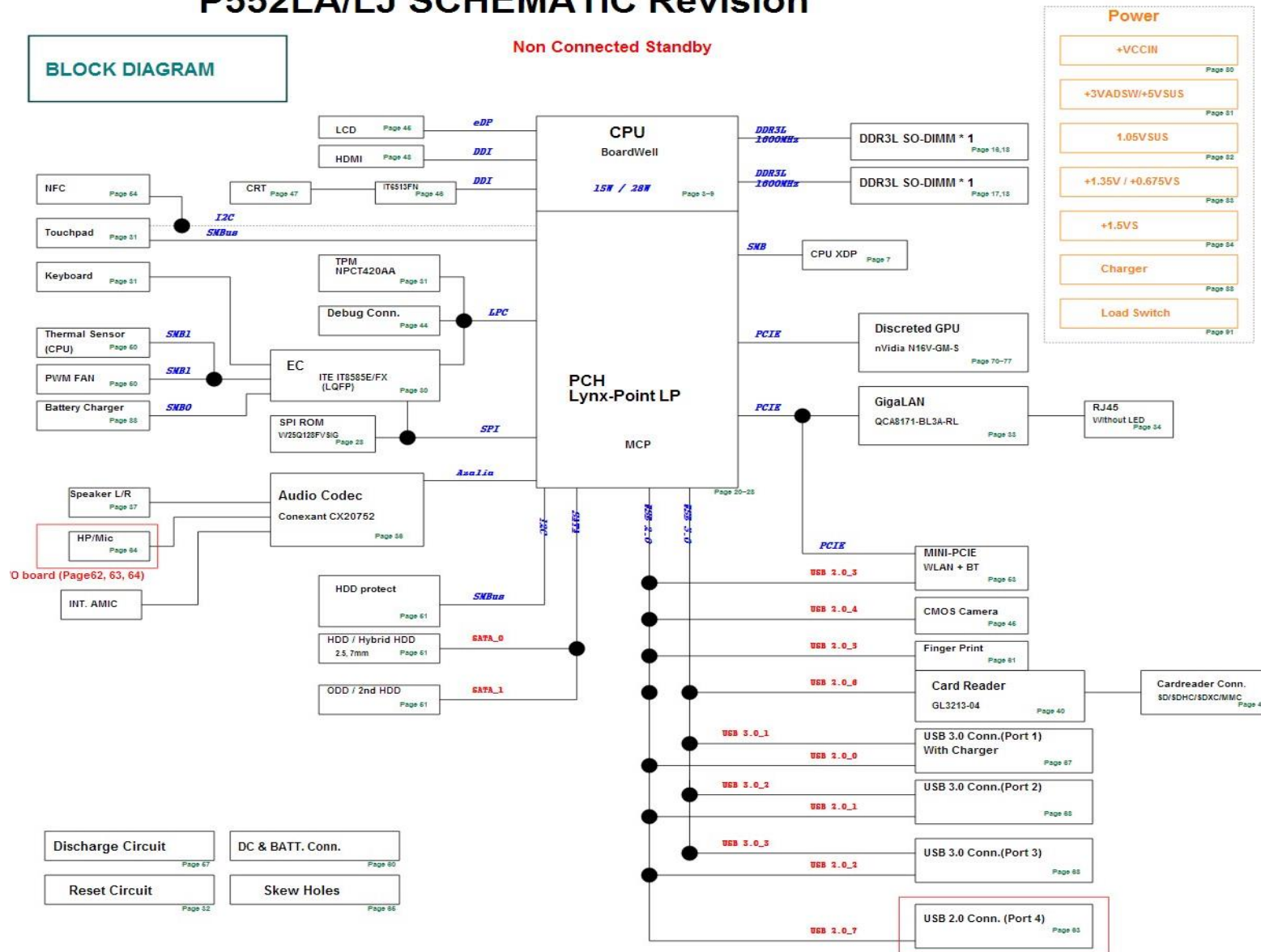
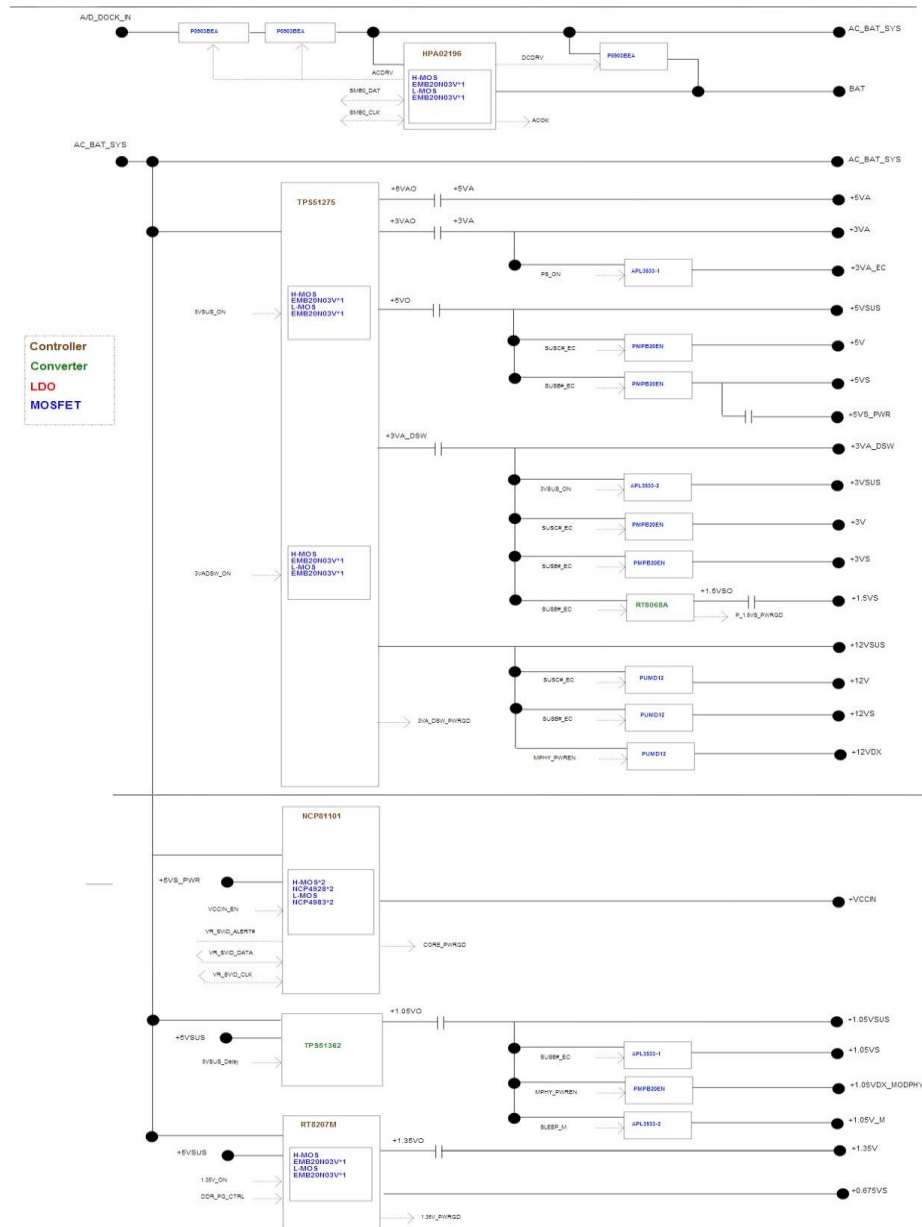


## BLOCK DIAGRAM

### P552LA/LJ SCHEMATIC Revision



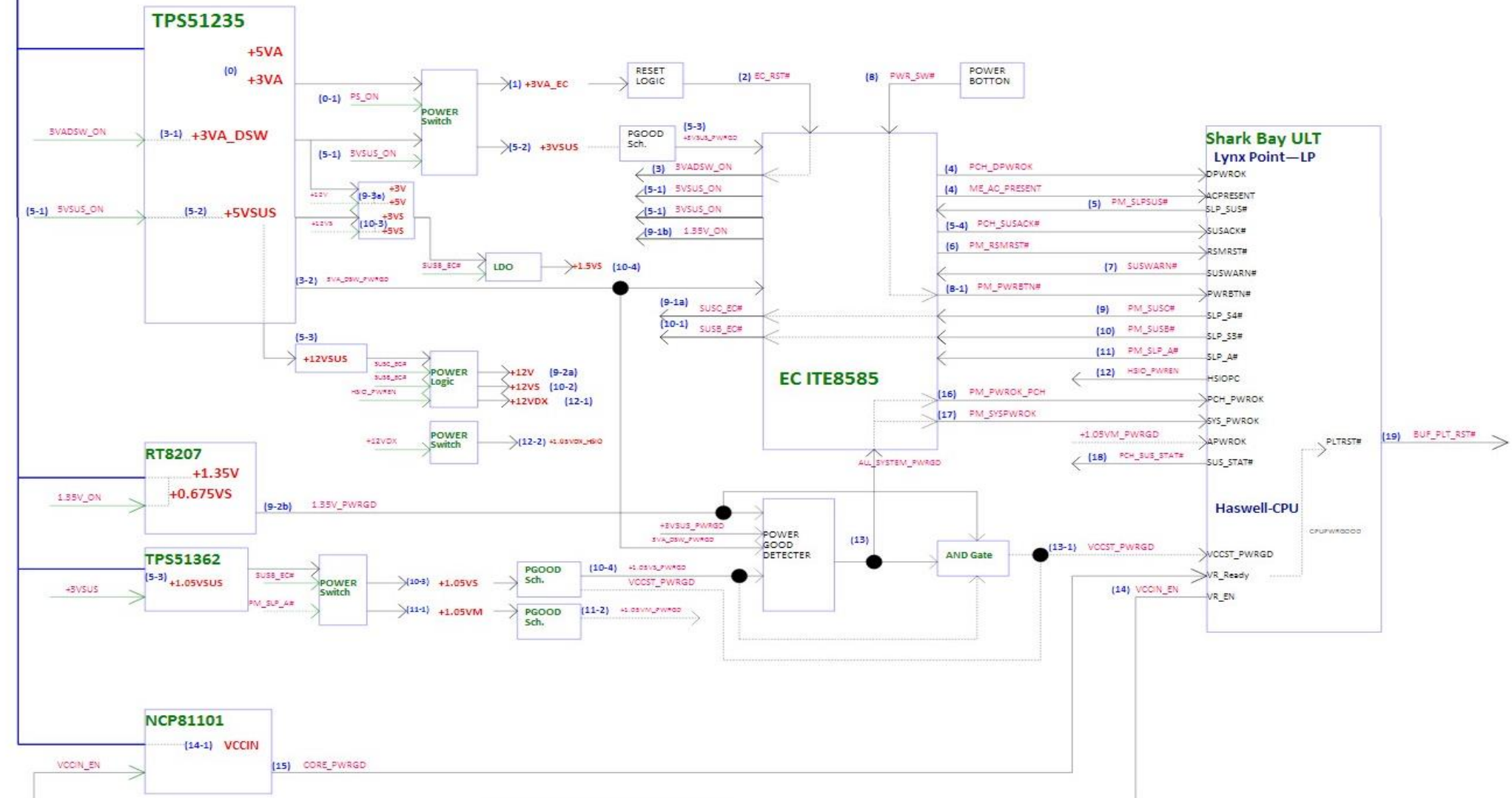
## POWER FLOW



# POWER ON SEQUENCE

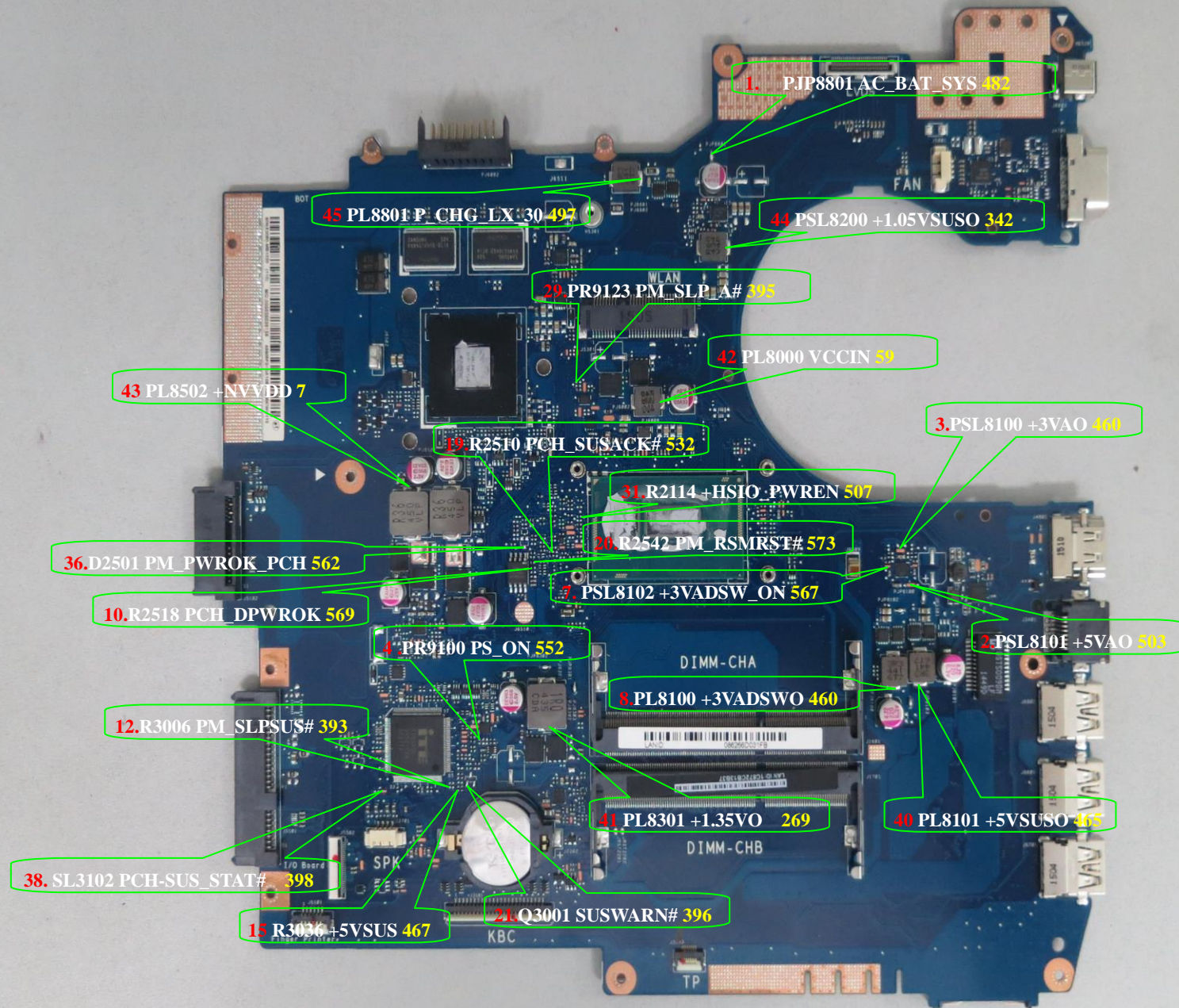
AC\_BAT\_SYS

PU551 Power On Sequence Diagram Rev.1.0





## Signal Measure Point-Bottom





## Signal Measure Point-Top

